

Features

- Extremely Low Noise Figure
- Low Gain
- High Intercept Point
- Internal Regulator/Active Bias

Options

- Lower Noise Figure
- Other Bandwidths
- Gain Levels
- Higher Ip3

Low Noise Amplifiers

PS1031



Description

Designed for front end performance in the cellular band, this family of amplifiers utilizes GaAs FET devices to achieve low noise and high third order intercept point.

Model	PS1031	Units
Frequency	806-850	MHz
Gain (min)	9	dB
Flatness p-p (max)	0.6	dB
Flatness p-p (806-824 MHz)	0.25	dB
NF (max/typ)	0.8/0.7	dB
VSWR in (max)	1.5:1	
VSWR out (max)	1.5:1	
P1dB (min)	+12	dBm
Output Ip3 (min)	+22	dBm
Voltage	+15 to +24	VDC
Current	42 +/- 5	mA
Dimensions	3.0 x 1.5 x .75	inches

Specification at T = +25°C

Operating temperature: -10 to +50°C.

Storage temperature: -30 to +70°C

Input/Output Impedance: 50 ohm

Input/Output Conn: SMA "F"



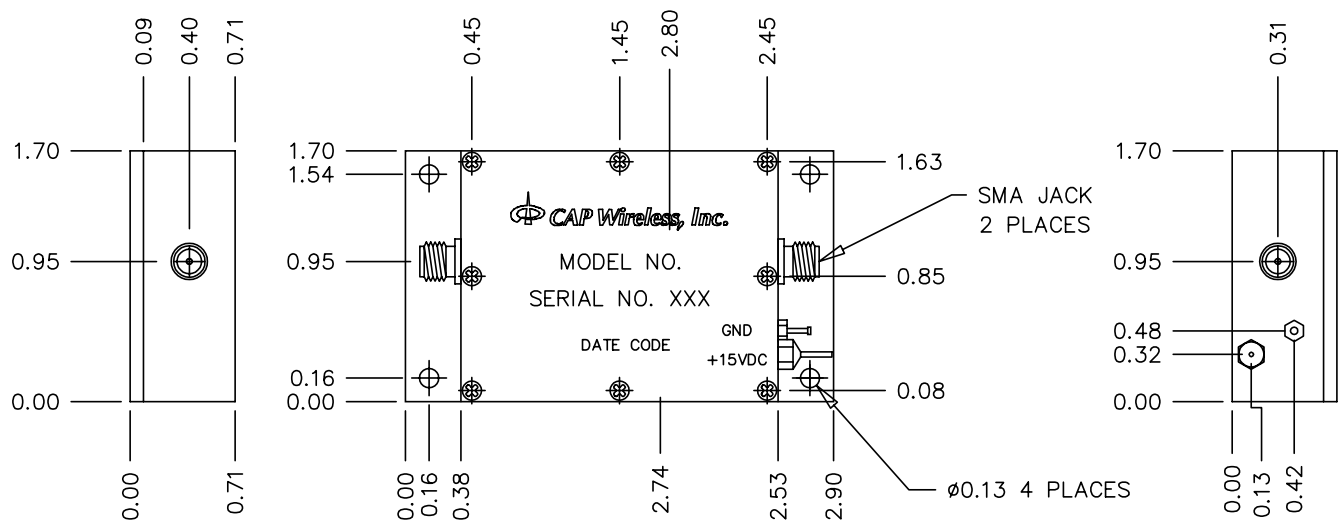
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Low Noise Amplifiers

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Outline Drawing



Company Design Philosophy

Essential to the company's strategy is the use of the latest and most sophisticated design software available. These design tools include complete suites of HP-EEsof, and AWR- Microwave Office, circuit and system high frequency EDA tools. The company consistently achieves its goal of accurately creating "prototypes" in software, as evidenced by its ability to go directly from a simulated design to deliverable prototypes and rapidly ramp to fulfill volume requirements. A crucial element of the company's development philosophy is to "design for production" to drastically improve manufacturability by virtually eliminating tuning and adjustments as part of the manufacturing process. The result is lower cost, higher reliability products with predictable delivery times.

The products shown on these data sheets are merely a representation of the company's capabilities, where a library of designs is available to draw upon to meet specific customer performance requirements. If you have a unique requirement, contact the factory to explore the latest in technology.